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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known **Application Number** 10/077,258 **Filing Date** February 14, 2002 Mo, Brian Sze-Ki, et. al. **First Named Inventor** Art Unit 2829 **Examiner Name** Jimmy Nguyen

SB/08B (10-0

(use as many sheets as necessary)

Sheet of 018865-001010US Attorney Docket Number

			U.S. PATENT D	OCUMENTS	
		Document Number			
Examiner	Cite No.1	Number Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
y E	AA	US-4,326,332	04/27/1982	Kenney	
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NR	AM	JP	63288047	Α	11/25/1988	Toshiba Corp.		
<u> </u>	AN	JP	6422051	A	01/25/1989	Matsushita Electric IND Co LTD		

OTHER PRIOR ART NON PATENT LITERATURE DOCUMENTS						
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²			
R	AO	KAO et al., "Two-Dimensional Thermal Oxidation of Silicon-I. Experiments," IEEE Transactions on Electron Devices, Vol. ED-34, No. 5, pp. 1008-1017, (May 1997)				
Y	AP	KAO et al., "Two-Dimensional Thermal Oxidation of Silicon-II. Modeling Stress Effects in Wet Oxides," IEEE Transactions on Electron Devices, Vol. ED-35, No. 1, pages. 14, (January 1988)				

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Examiner Signature	WM.	Date Considered	9/28/02

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